

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Robert Madge et. al.

Serial No. : 10/020,407

Filed : December 12, 2001

For : METHOD OF DETECTING SPATIALLY CORRELATED VARIATIONS IN A PARAMETER OF AN INTEGRATED CIRCUIT DIE

Group Art Unit: 2822

Examiner : Trinh, M.

Atty Docket : 71739 (6653) / 01-299

STATUS REQUEST

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

Respectfully submitted,

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FAXDate February 5, 2004Number of pages including cover sheet 2
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 LSI Logic Corporation
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To: US PTO
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TC 2822
Fax No. 703-872-9306
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REMARKS:

Urgent For your review Reply ASAP Please comment

Application Number: 10/020,407
Filing date: December 12, 2001
First named inventor: Robert Madge et al.
Attorney docket number: 01-299 / 71739(6653)

Transmitted herewith for filing via facsimile:

- Status Request Letter

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